

## LM556/LM556C Dual Timer

### General Description

The LM556 Dual timing circuit is a highly stable controller capable of producing accurate time delays or oscillation. The 556 is a dual 555. Timing is provided by an external resistor and capacitor for each timing function. The two timers operate independently of each other sharing only  $V_{CC}$  and ground. The circuits may be triggered and reset on falling waveforms. The output structures may sink or source 200 mA.

### Features

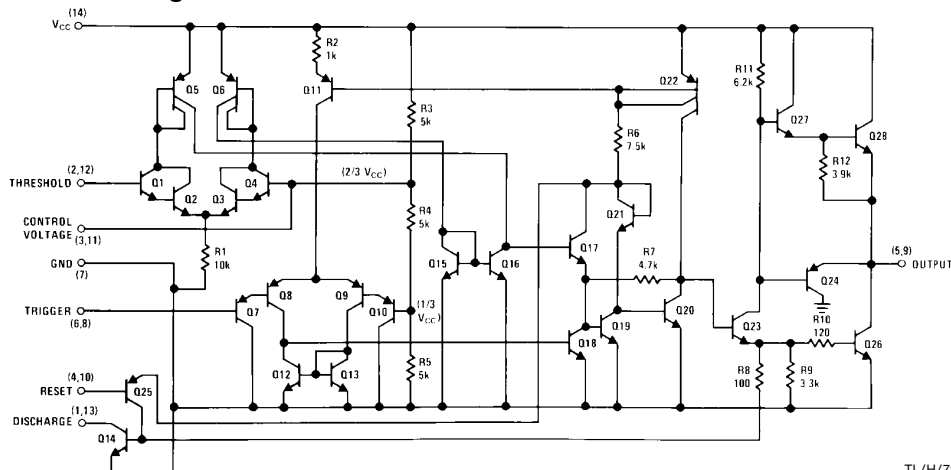
- Direct replacement for SE556/NE556
- Timing from microseconds through hours
- Operates in both astable and monostable modes
- Replaces two 555 timers

- Adjustable duty cycle
- Output can source or sink 200 mA
- Output and supply TTL compatible
- Temperature stability better than 0.005% per °C
- Normally on and normally off output

### Applications

- Precision timing
- Pulse generation
- Sequential timing
- Time delay generation
- Pulse width modulation
- Pulse position modulation
- Linear ramp generator

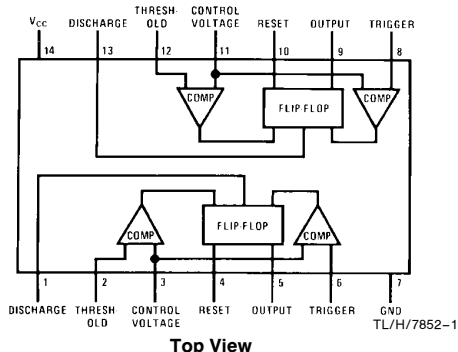
### Schematic Diagram



TL/H/7852-2

### Connection Diagram

#### Dual-In-Line and Small Outline Packages



Top View

Order Number LM556J or LM556CJ  
See NS Package Number J14A

Order Number LM556CM  
See NS Package Number M14A

Order Number LM556CN  
See NS Package Number N14A

## Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	+18V
Power Dissipation (Note 1)	
LM556J, LM556CJ	1785 mW
LM556CN	1620 mW
Operating Temperature Ranges	
LM556C	0°C to +70°C
LM556	−55°C to +125°C

Storage Temperature Range −65°C to +150°C

Soldering Information	
Dual-In-Line Package	
Soldering (10 seconds)	260°C
Small Outline Package	
Vapor phase (60 seconds)	215°C
Infrared (15 seconds)	220°C

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

## Electrical Characteristics (T<sub>A</sub> = 25°C, V<sub>CC</sub> = +5V to +15V, unless otherwise specified)

Parameter	Conditions	LM556			LM556C			Units
		Min	Typ	Max	Min	Typ	Max	
Supply Voltage		4.5		18	4.5		16	V
Supply Current (Each Timer Section)	V <sub>CC</sub> = 5V, R <sub>L</sub> = ∞ V <sub>CC</sub> = 15V, R <sub>L</sub> = ∞ (Low State) (Note 2)		3 10	5 11		3 10	6 14	mA mA
Timing Error, Monostable Initial Accuracy Drift with Temperature	R <sub>A</sub> = 1k to 100 kΩ, C = 0.1 μF, (Note 3)		0.5 30			0.75 50		% ppm/°C
Accuracy over Temperature Drift with Supply			1.5 0.05			1.5 0.1		% %/V
Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply			1.5 90 2.5 0.15			2.25 150 3.0 0.30		% ppm/°C % %/V
Trigger Voltage	V <sub>CC</sub> = 15V V <sub>CC</sub> = 5V	4.8 1.45	5 1.67	5.2 1.9	4.5 1.25	5 1.67	5.5 2.0	V V
Trigger Current			0.1	0.5		0.2	1.0	μA
Reset Voltage	(Note 4)	0.4	0.5	1	0.4	0.5	1	V
Reset Current			0.1	0.4		0.1	0.6	mA
Threshold Current	V <sub>TH</sub> = V-Control (Note 5) V <sub>TH</sub> = 11.2V		0.03	0.1 250		0.03	0.1 250	μA nA
Control Voltage Level and Threshold Voltage	V <sub>CC</sub> = 15V V <sub>CC</sub> = 5V	9.6 2.9	10 3.33	10.4 3.8	9 2.6	10 3.33	11 4	V V
Pin 1, 13 Leakage Output High			1	100		1	100	nA
Pin 1, 13 Sat Output Low Output Low	(Note 6) V <sub>CC</sub> = 15V, I = 15 mA V <sub>CC</sub> = 4.5V, I = 4.5 mA		150 70	240 100		180 80	300 200	mV mV

## Electrical Characteristics (T<sub>A</sub> = 25°C, V<sub>CC</sub> = +5V to +15V, unless otherwise specified) (Continued)

Parameter	Conditions	LM556			LM556C			Units
		Min	Typ	Max	Min	Typ	Max	
Output Voltage Drop (Low)	V <sub>CC</sub> = 15V							
	I <sub>SINK</sub> = 10 mA		0.1	0.15		0.1	0.25	V
	I <sub>SINK</sub> = 50 mA		0.4	0.5		0.4	0.75	V
	I <sub>SINK</sub> = 100 mA		2	2.25		2	2.75	V
	I <sub>SINK</sub> = 200 mA		2.5			2.5		V
	V <sub>CC</sub> = 5V							
Output Voltage Drop (High)	I <sub>SOURCE</sub> = 200 mA, V <sub>CC</sub> = 15V		12.5			12.5		V
	I <sub>SOURCE</sub> = 100 mA, V <sub>CC</sub> = 15V	13	13.3		12.75	13.3		V
	V <sub>CC</sub> = 5V	3	3.3		2.75	3.3		V
Rise Time of Output			100			100		ns
Fall Time of Output			100			100		ns
Matching Characteristics	(Note 7)							
Initial Timing Accuracy			0.05	0.2		0.1	2.0	%
Timing Drift with Temperature			± 10			± 10		ppm/°C
Drift with Supply Voltage			0.1	0.2		0.2	0.5	%/V

**Note 1:** For operating at elevated temperatures the device must be derated based on a +150°C maximum junction temperature and a thermal resistance of 70°C/W (Ceramic), 77°C/W (Plastic DIP) and 110°C/W (SO-14 Narrow).

**Note 2:** Supply current when output high typically 1 mA less at V<sub>CC</sub> = 5V.

**Note 3:** Tested at V<sub>CC</sub> = 5V and V<sub>CC</sub> = 15V.

**Note 4:** As reset voltage lowers, timing is inhibited and then the output goes low.

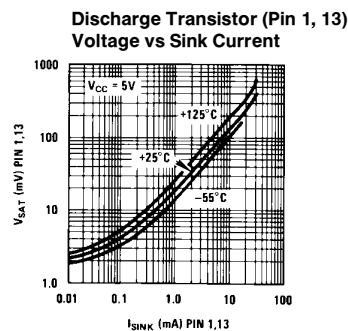
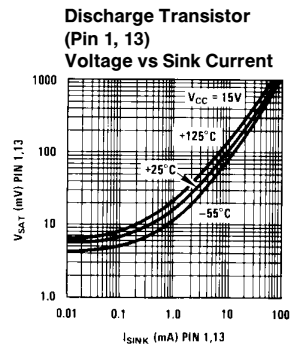
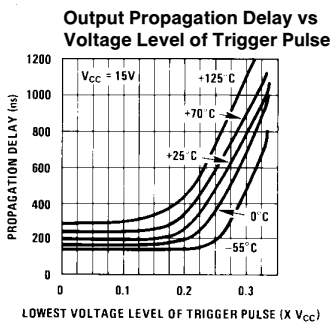
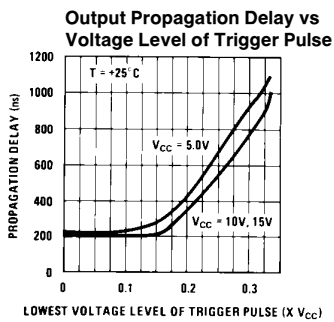
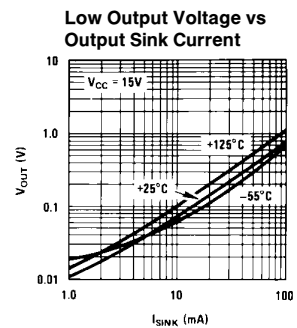
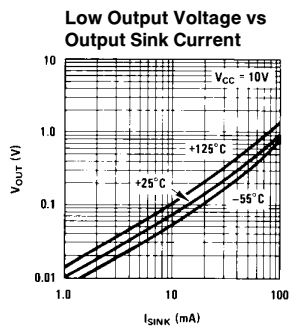
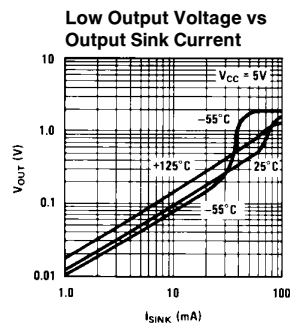
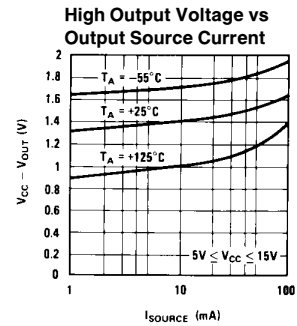
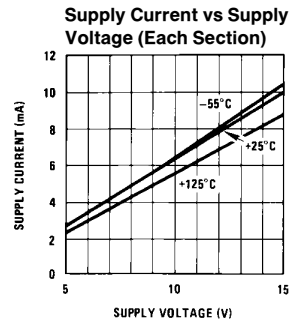
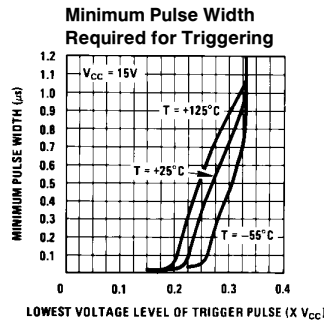
**Note 5:** This will determine the maximum value of R<sub>A</sub> + R<sub>B</sub> for 15V operation. The maximum total (R<sub>A</sub> + R<sub>B</sub>) is 20 MΩ.

**Note 6:** No protection against excessive pin 1, 13 current is necessary providing the package dissipation rating will not be exceeded.

**Note 7:** Matching characteristics refer to the difference between performance characteristics of each timer section.

**Note 8:** Refer to RETS556X drawing for specifications of military LM556J version.

## Typical Performance Characteristics



TL/H/7852-3

Technical drawing of a 14-pin D-sub connector showing top, side, and detail views with dimensions in inches and millimeters.

**Top View Dimensions:**

- Overall Width: 0.785 (19.939) MAX
- Pin Pitch (between pins 14 and 8): 0.025 (0.635) RAD
- Pin Pitch (between pins 1 and 7): 0.220-0.310 (5.588-7.874)
- Pin Numbers: 14, 13, 12, 11, 10, 9, 8 (top); 1, 2, 3, 4, 5, 6, 7 (bottom)

**Side View Dimensions:**

- Overall Height: 0.180 (4.572) MAX
- Pin Height: 0.290-0.320 (7.366-8.128)
- Pin Angle: 95° ±5°
- Pin Spacing (between pins 14 and 8): 0.310-0.410 (7.874-10.41)
- Pin Angle: 10° MAX
- Pin Spacing (between pins 1 and 7): 0.310-0.410 (7.874-10.41)

**Detail View Dimensions (Pin 1):**

- Pin Height: 0.005 (0.127) MIN
- Pin Angle: 86°-94° TYP
- Pin Spacing (between pins 14 and 8): 0.060 ±0.005 (1.524 ±0.127)
- Pin Spacing (between pins 1 and 7): 0.100 ±0.010 (2.540 ±0.254)
- Pin Spacing (between pins 14 and 8): 0.018 ±0.003 (0.457 ±0.076)
- Pin Spacing (between pins 1 and 7): 0.125-0.200 (3.175-5.080)
- Pin Spacing (between pins 14 and 8): 0.020-0.060 (0.508-1.524)
- Pin Spacing (between pins 1 and 7): 0.150 (3.81) MIN

**Other Dimensions:**

- Pin Spacing (between pins 14 and 8): 0.008-0.012 (0.203-0.305)
- Pin Spacing (between pins 1 and 7): 0.098 (2.489) MAX BOTH ENDS
- Pin Spacing (between pins 14 and 8): 0.200 (5.080) MAX
- Pin Spacing (between pins 1 and 7): 0.125-0.200 (3.175-5.080)
- Pin Spacing (between pins 14 and 8): 0.020-0.060 (0.508-1.524)
- Pin Spacing (between pins 1 and 7): 0.150 (3.81) MIN

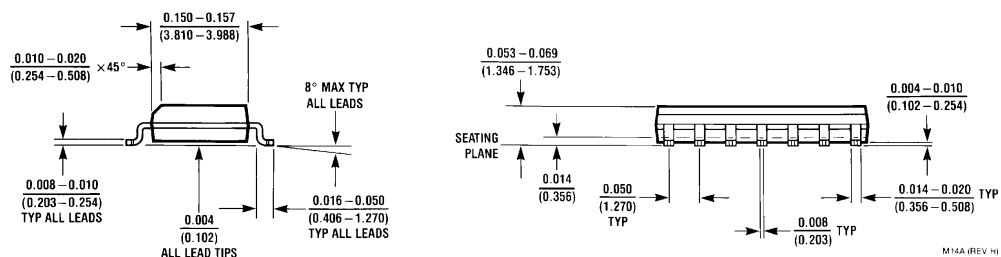
0.335 - 0.344  
(8.509 - 8.738)

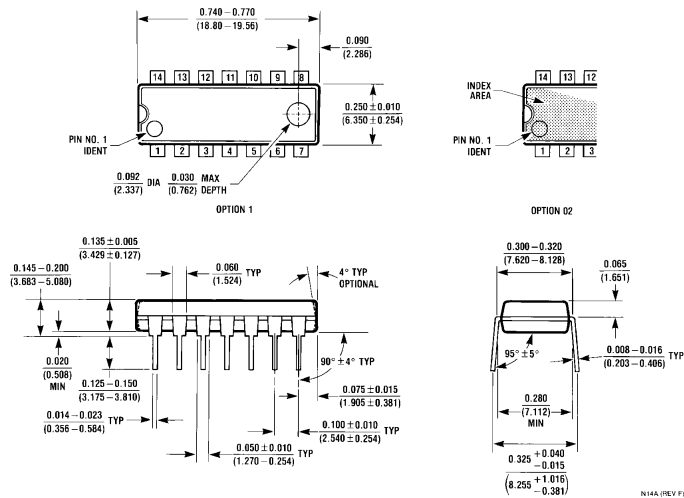
0.228 - 0.244  
(5.791 - 6.198)

LEAD NO. 1  
IDENT

30°  
TYP

0.010 MAX  
(0.254)



**Physical Dimensions** inches (millimeters) (Continued)

**Molded Dual-In-Line Package (N)**  
**Order Number LM556CN**  
**NS Package Number N14A**

**LIFE SUPPORT POLICY**

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



**National Semiconductor Corporation**  
 1111 West Bardin Road  
 Arlington, TX 76017  
 Tel: 1(800) 272-9959  
 Fax: 1(800) 737-7018

**National Semiconductor Europe**  
 Fax: (+49) 0-180-530 85 86  
 Email: cnjwge@tevm2.nsc.com  
 Deutsch Tel: (+49) 0-180-530 85 85  
 English Tel: (+49) 0-180-532 78 32  
 Français Tel: (+49) 0-180-532 93 58  
 Italiano Tel: (+49) 0-180-534 16 80

**National Semiconductor Hong Kong Ltd.**  
 13th Floor, Straight Block,  
 Ocean Centre, 5 Canton Rd.  
 Tsimshatsui, Kowloon  
 Hong Kong  
 Tel: (852) 2737-1600  
 Fax: (852) 2736-9960

**National Semiconductor Japan Ltd.**  
 Tel: 81-043-299-2309  
 Fax: 81-043-299-2408

National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications.

This datasheet has been download from:

[www.datasheetcatalog.com](http://www.datasheetcatalog.com)

Datasheets for electronics components.